

CHAUDHARY DEVI LAL UNIVERSITY, SIRSA
(Established by the State Legislature Act 9 of 2003)



NOTIFICATION

(Through email)

The Vice-Chancellor has pleased to constitute the following Adhoc Board (common for UG/PG) for the Electronics & Communication Engineering:

Electronics & Communication Engineering:

Sr. no.	Proposed	Designation
(i)	Dean Faculty of Engineering & Technology, CDLU, Sirsa.	Chairperson/ Member
(ii)	Two teachers of Subject from the affiliated Colleges: 1. Dr. Dinesh Gupta, Principal, JCDL Memorial College of Engineering, Barnala Road, Sirsa. (Term for two years w.e.f. date of notification). 2. Ms. Priyanka Handa, Asstt. Prof., CDL State Institute of Eng. & Tech. Panniwala Mota, Sirsa. (Term for two years w.e.f. date of notification).	Member Member
(iii)	One Outside Subject Expert: Dr. Suman Dahiya, Associate Professor, Department of Electronics & Communication Engineering, GJUS&T, Hisar. Email: <u>suman.23 ahilya@yahoo.co.in</u> (Term for two years w.e.f. date of notification).	Member


03/02/2023
Deputy Registrar (Acad.)
For Registrar

Endst. No./Acad./AC-II/70/23/7209-7212

dated: 03/2/23

Copy of the above is forwarded to the following for information and further necessary action:-

1. All the members of the above committee.
2. The Dean, Faculty of Engg. & Technology, Chaudhary Devi Lal University, Sirsa.
3. The Chairperson, Department of CSE, Chaudhary Devi Lal University, Sirsa.
4. The Principal, JCDL Memorial College of Engineering, Barnala Road, Sirsa.
5. The Principal, CDL State Institute of Eng. & Tech. Panniwala Mota, Sirsa.
5. The Director, UITDC, CDLU, Sirsa with the request to upload the above notification on the University Website
6. P.S. to Vice-Chancellor (for kind information of the Vice-Chancellor), Chaudhary Devi Lal University, Sirsa.
7. P.A. to Registrar (for kind information of the Registrar), Chaudhary Devi Lal University, Sirsa.


03/02/23
Superintendent (Acad.)